

asureVIP™ from TVS

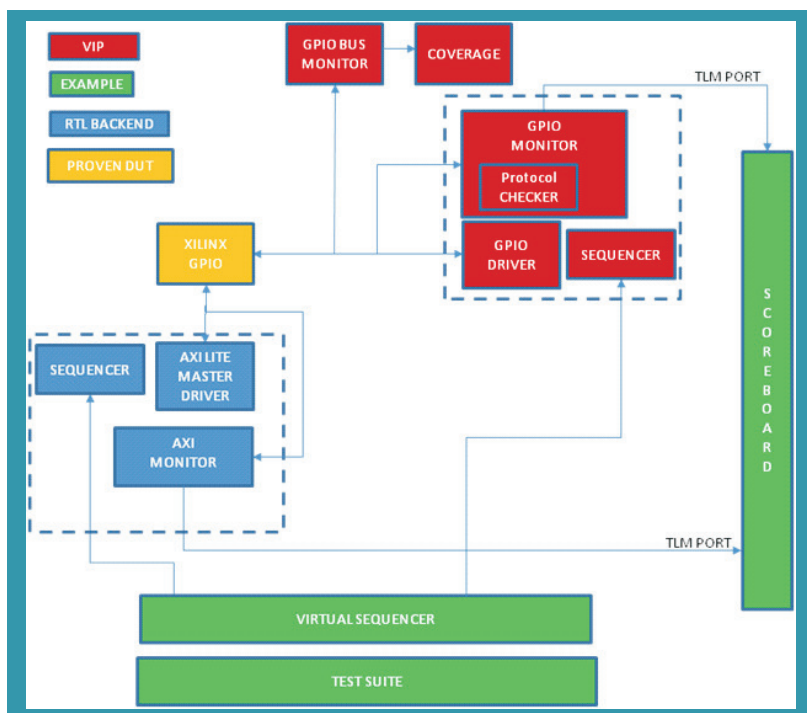
TVS GPIO OVM VIP

OVERVIEW

Test and Verification Solutions offers a GPIO OVM VIP as part of its asureVIP™ series of offerings. This is a highly flexible and configurable verification IP, which can be easily integrated into any SOC verification environment. The TVS GPIO OVM VIP supports Bidirectional Ports. It was used in successfully verifying a DUT, later silicon proven.

The VIP comes with a Bus Monitor for performing all required Checks.

BLOCK DIAGRAM



Test and Verification Solutions

FEATURES

- Configurable single or dual GPIO channel(s)
- Configurable channel width for GPIO pins from 1-bit to 32 bits
- Each GPIO bit can be configured as input or output
- Supports individual configuration of each channel
- Supports n-bits of transfer



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TECHNICAL SPECIFICATION

Part Number	asureVIP_GPIO
Description	GPIO OVM VIP
Provider	Test and Verification Solutions
Languages Supported	System Verilog
Methodology	OVM 2.1.1
Simulators	Cadence Incisive, Mentor Questa
Compliance	Not Applicable
Availability	August 2011

ABOUT US

TVS delivers an independent verification service that not only reduces your costs and time-to-market, but also improves product quality.

TVS combines skills and experience in software testing, hardware verification and outsourcing to provide customers with an efficient, well-managed, quality assurance service.

TVS provides both consultancy and execution services using experienced engineering resources in several locations around the world. TVS removes the pain and risk from outsourcing leaving you with just the benefits.

To learn more about our offerings, write to us at vip@tandvsolns.co.uk

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BENEFITS

- Highly Flexible, Independent and Configurable GPIO VIP
- Proven against Silicon Proven VIP
- Less TAT in integrating into SOC Verification environments

DELIVERABLES

- VIP user Guide
- GPIO OVM VIP
- Sample Testbench Integrated with proven XILINX GPIO VIP
- Sample Scoreboard
- Sample Virtual Sequencer



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